

-
- FC5A MicroSmart PID
 - MicroSmart IDEC MicroSmart MicroSmart
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- MicroSmart MicroSmart
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- MicroSmart “ 2” 2 IEC 60664-1 MicroSmart
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- MicroSmart IEC 60127 MicroSmart
- IEC60127 MicroSmart
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- MicroSmart M4 MicroSmart
 MicroSmart

- MicroSmart

- MicroSmart

FC5A MicroSmart PID

FC5A MicroSmart

FC5A MicroSmart PID

1.	IDEC
2.	
3.	

MicroSmart

MicroSmart FC5A MicroSmart		FC5A MicroSmart pentra			
FC5 CPU		FC5A-C10R2 FC5A-C10R2C FC5A-C16R2 FC5A-C16R2C FC5A-C24R2 FC5A-C24R2C			
		FC5A-D16RK1 FC5A-D16RS1 FC5A-D32K3 FC5A-D32S3			
PID		FC5A-F2MR2 FC5A-F2M2			
		FC5A-SIF2 FC5A-SIF4			
		FC4A-PM32 FC4A-PM64			
		I/O			
I/O		I/O			
		AS-Interface			
		HMI RS232C RS485			
		HMI		RS232C	RS485
WindLDR		[WindLDR]			
FC5A		FC5A MicroSmart pentra FC9Y-B1269 FC9Y-B1274			



FC5A CPU PID FC5A CPU

IDEC	IDEC PLC
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2011 3	FC9Y-B1284-0	

FC5A MicroSmart

FC9Y B1284	FC5A PID	PID
FC9Y B1269	FC5A MicroSmart Pentra	Modbus ASCII/RTU
FC9Y B1274	FC5A MicroSmart Pentra	<p>/</p> <p>RS232C/RS485 AS - Interface PID /</p> <p>Modbus TCP</p>
FC9Y B1279	FC5A MicroSmart Pentra Web CPU	FC5A CPU

1:	1-1
	PID	1-1
	PID	1-1
	CPU WindLDR	1-2
	1-2
2:	2-1
	PID	2-1
	2-3
	2-6
3:	3-1
	3-1
	3-3
	3-4
	3-5
	PID	3-6
4: PID	4-1
	PID	4-1
	4-3
	(AT)/	4-6
	4-9
	/	4-14
	4-14
	4-15
5: PID	5-1
	PID	5-1
	5-2
	5-2
	5-2
	5-3
	- 0	5-7
	- 1	5-10
	- 2 3 SHOT	5-17
	- 4 5 SHOT	5-19
	- 10-19 CH0 SHOT	5-22
	- 30-39 CH1 SHOT	5-24

6:	WINDLDR	PID	6-1
	PID		6-1
			6-6
	PID		6-7
	PID	-	CH0 CH1	6-8
	PID	-	CH0 CH1	6-13
	PID	-	CH0 CH1	6-17
	PID	-	CH0 CH1	6-19
	PID	- I/O	6-21
	PID	-	6-24
	PID	-	6-34
	PID	-	6-45
	PID	-	6-47
	PID		6-52
7:			7-1
	1		7-1
	2		7-8
	3		7-15
8:			8-1
	PID	LED(PWR)	8-1
	PID		8-2
	ON/OFF		8-3
	PID	PI PD P	8-3
	PID		8-4
			8-6
			8-6
9:			9-1
	PID		9-1
			9-5
	PID		9-9

1:

FC5A PID

PID

PID

PID (SP) (PV) PID
 FC5A CPU CPU CPU 12VDC CPU CPU
 CPU 24- I / O CPU 12VDC CPU
 PID WindLDR
 PID

PID

	I/O	I/O	
	2	[K, J, R, S, B, E, T, N, PL- II, C (W/Re5-26)] (Pt100, JPt100) (0 1V DC 0 5V DC 1 5V DC 0 10V DC) (0 20mA DC 4 20mA DC)	FC5A-F2MR2
	2		
SSR /	2	[K, J, R, S, B, E, T, N, PL- II, C (W/Re5-26)] (Pt100, JPt100) (0 1V DC 0 5V DC 1 5V DC 0 10V DC) (0 20mA DC 4 20mA DC)	FC5A-F2M2
	2	SSR /	

PID

MicroSmart CPU PID CPU PID

FC5A MicroSmart CPU	FC5A-C10R2 FC5A-C10R2C FC5A-C10R2D	FC5A-C16R2 FC5A-C16R2C FC5A-C16R2D FC5A-C24R2D	FC5A-C24R2 FC5A-C24R2C	FC5A-D16RK1 FC5A-D16RS1 FC5A-D32K3 FC5A-D32S3 FC5A-D12K1E FC5A-D12S1E
PID	—		4	7

CPU WindLDR

PID	FC5A CPU	WindLDR		
FC5A MicroSmart CPU	FC5A-C10R2 FC5A-C10R2C FC5A-C10R2D	FC5A-C16R2 FC5A-C16R2C FC5A-C16R2D FC5A-C24R2D	FC5A-C24R2 FC5A-C24R2C	FC5A-D16RK1 FC5A-D16RS1 FC5A-D32K3 FC5A-D32S3 FC5A-D12K1E FC5A-D12S1E
CPU	—		230	*1
WindLDR			6.40	

*1 PID 100 FC5A-D12K1E/-S1E

WindLDR

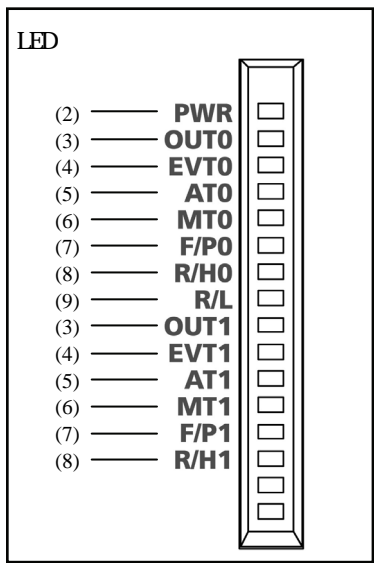
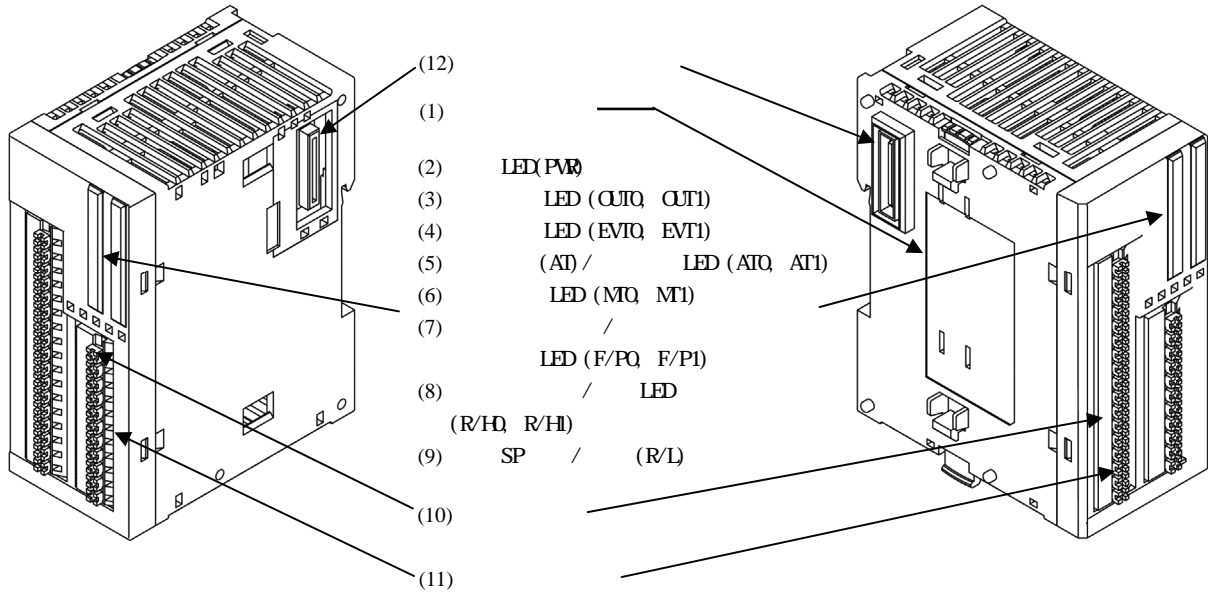
1. FC5A-D12x1E CPU I/F (FC2A-KC4C) USB HG9Z-XCM2A PC FC5A
CPU 1 2
2. WindLDR **Online > Monitor**
3. WindLDR **PLC > Status** PLC



2:

PID

PID



(1)

PID

(2) **LED(PWR)**

(24V DC)

(3) **LED (OUT0, OUT1)**

LED (MV) 20% LED 25 125 100

(4) **LED (EVT0, EVT1)**

1 8

(5) **(AT)/ LED (AT0, AT1)**

(AT)
(AT)

(6) **LED (MT0, MT1)**

(7) / **LED (F/P0, F/P1)**

(8) / **LED (R/H0, R/H1)**

(9) **SP / LED (R/L)**

(10)

(11)

(12) CPU

PID

	FC5A-F2MR2	FC5A-F2M2																																															
	<table border="1"> <thead> <tr> <th></th> <th></th> <th>LSB</th> </tr> </thead> <tbody> <tr> <td>K</td> <td>-200 1370°C</td> <td>-328 2498°F</td> <td>1°C (°F)</td> </tr> <tr> <td>K</td> <td>-200.0 400.0°C</td> <td>-328.0 752. °F</td> <td>0.1°C (°F)</td> </tr> <tr> <td>J</td> <td>-200 1000°C</td> <td>-328 1832°F</td> <td>1°C (°F)</td> </tr> <tr> <td>R</td> <td>0 1760°C</td> <td>32 3200°F</td> <td>1°C (°F)</td> </tr> <tr> <td>S</td> <td>0 1760°C</td> <td>32 3200°F</td> <td>1°C (°F)</td> </tr> <tr> <td>B</td> <td>0 1820°C</td> <td>32 3308°F</td> <td>1°C (°F)</td> </tr> <tr> <td>E</td> <td>-200 800°C</td> <td>-328 1472°F</td> <td>1°C (°F)</td> </tr> <tr> <td>T</td> <td>-200.0 400.0°C</td> <td>-328.0 752.0°F</td> <td>0.1°C (°F)</td> </tr> <tr> <td>N</td> <td>-200 1300°C</td> <td>-328 2372°F</td> <td>1°C (°F)</td> </tr> <tr> <td>PL-II</td> <td>0 1390°C</td> <td>32 2534°F</td> <td>1°C (°F)</td> </tr> <tr> <td>C (W/Re5-26)</td> <td>0 2315°C</td> <td>32 4199°F</td> <td>1°C (°F)</td> </tr> </tbody> </table>				LSB	K	-200 1370°C	-328 2498°F	1°C (°F)	K	-200.0 400.0°C	-328.0 752. °F	0.1°C (°F)	J	-200 1000°C	-328 1832°F	1°C (°F)	R	0 1760°C	32 3200°F	1°C (°F)	S	0 1760°C	32 3200°F	1°C (°F)	B	0 1820°C	32 3308°F	1°C (°F)	E	-200 800°C	-328 1472°F	1°C (°F)	T	-200.0 400.0°C	-328.0 752.0°F	0.1°C (°F)	N	-200 1300°C	-328 2372°F	1°C (°F)	PL-II	0 1390°C	32 2534°F	1°C (°F)	C (W/Re5-26)	0 2315°C	32 4199°F	1°C (°F)
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	() 15V DC																																																
	100																																																
	24V DC	5V DC																																															
	20.4 28.8V DC																																																

	FC5A-F2MR2	FC5A-F2M2				
	<table border="1"> <tr> <td></td> <td>F6018-17P (Fujicon) F6018-11P (Fujicon)</td> </tr> <tr> <td>/</td> <td>—</td> </tr> </table>			F6018-17P (Fujicon) F6018-11P (Fujicon)	/	—
	F6018-17P (Fujicon) F6018-11P (Fujicon)					
/	—					

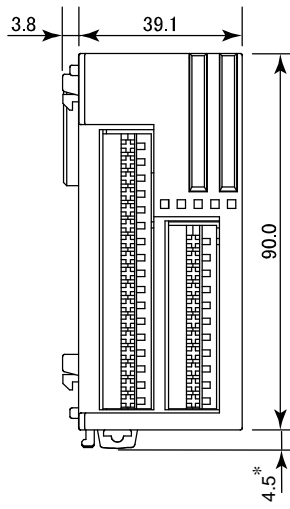
	FC5A-F2MR2	FC5A-F2M2																
25°C	<table border="1"> <tr> <td></td> <td>±0.2% ±2°C</td> </tr> <tr> <td></td> <td>R S 0 200°C</td> </tr> <tr> <td></td> <td>±6°C</td> </tr> <tr> <td></td> <td>B 0 300°C</td> </tr> <tr> <td></td> <td>K J E T N 0°C</td> </tr> <tr> <td></td> <td>±0.4%</td> </tr> <tr> <td></td> <td>±0.1% ±1°C</td> </tr> <tr> <td></td> <td>±0.2%</td> </tr> </table>			±0.2% ±2°C		R S 0 200°C		±6°C		B 0 300°C		K J E T N 0°C		±0.4%		±0.1% ±1°C		±0.2%
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0 55°C	<table border="1"> <tr> <td></td> <td>±0.7%</td> </tr> <tr> <td></td> <td>R S 0 200°C</td> </tr> <tr> <td></td> <td>±6°C</td> </tr> <tr> <td></td> <td>B 0 300°C</td> </tr> <tr> <td></td> <td>K J E T N 0°C</td> </tr> <tr> <td></td> <td>±0.9%</td> </tr> <tr> <td></td> <td>±0.6%</td> </tr> <tr> <td></td> <td>±0.7%</td> </tr> </table>			±0.7%		R S 0 200°C		±6°C		B 0 300°C		K J E T N 0°C		±0.9%		±0.6%		±0.7%
	±0.7%																	
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	B 0 300°C																	
	K J E T N 0°C																	
	±0.9%																	
	±0.6%																	
	±0.7%																	
	25°C±																	
	0 55°C ±1°C																	
	125 ms																	

	FC5A-F2MR2	FC5A-F2M2
	1a	SSR
	5A 250V AC 5A 30V DC 3A 250V AC	12V DC±15%
	cos =0.4	4 20mA DC

	FC5A-F2MR2	FC5A-F2M2
	±0.5%	
	6	
	1,000,000	

	FC5A-F2MR2	FC5A-F2M2
	-	
	-	
	-	
	-	
	1500kV AC 5mA 1	
	-	
	1,500kV AC 5mA 1	
	-	
	1,500kV AC 5mA 1	FG -
	FG -	548V AC 5mA 1
	548V AC 5mA 1	I/O -
	-	548V AC 5mA 1
	548V AC 5mA 1	I/O -
	-	548V AC 5mA 1
	548V AC 5mA 1	-
	-	548V AC 5mA 1
	2500V AC 5mA 1	-
	-	548V AC 5mA 1
	2500V AC 5mA 1	
	-	
	548V AC 5mA 1	
	-	
	548V AC 5mA 1	

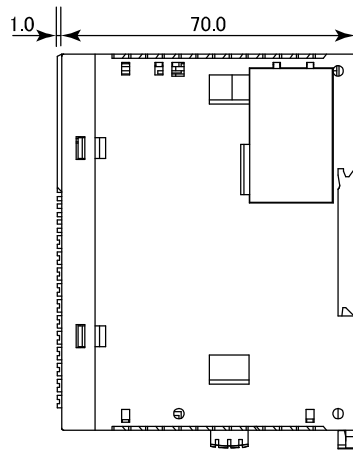
	FC5A-F2MR2	FC5A-F2M2
	3.5W	
5V DC	65mA	
24V DC	0mA	
	0 55°C	
	10 95%RH	
	140g	
	RoHS	



(mm)

*

8.5mm



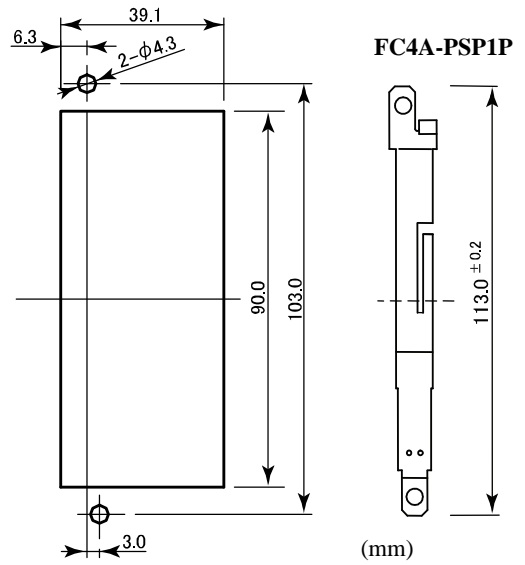
3:

PID
 PID FC5A MicroSmart (FC9Y-B1269) 3
 PID



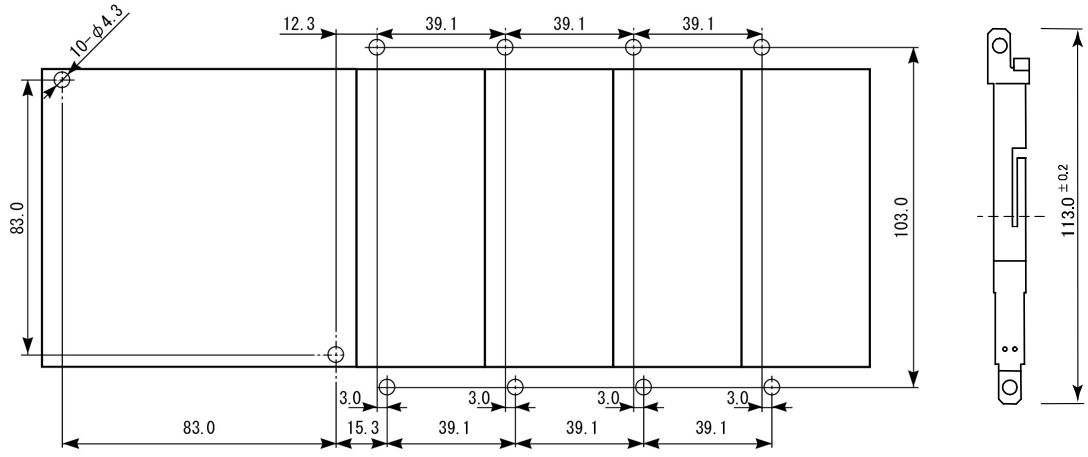
- DIN CPU PID
-
- FC5A MicroSmart

PID
 6 8 mm M4
 FC5A MicroSmart (FC9Y-B1269)



FC5A-C24R2

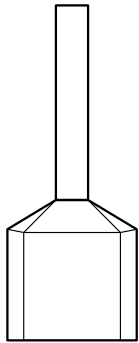
PID



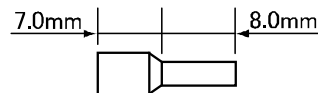
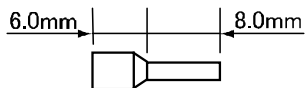
(mm)



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(CRIMFOX ZA 3)



Phoenix	
AI 1-8 RD	UL1007AWG18
AI 0.5-8 WH	UL1015AWG22

Phoenix	
AI-TWIN2x0.75-8 GY	UL1007AWG18
AI-TWIN2x0.5-8 WH	UL1015AWG22

Phoenix Contact

Phoenix Contact



IEC 60127

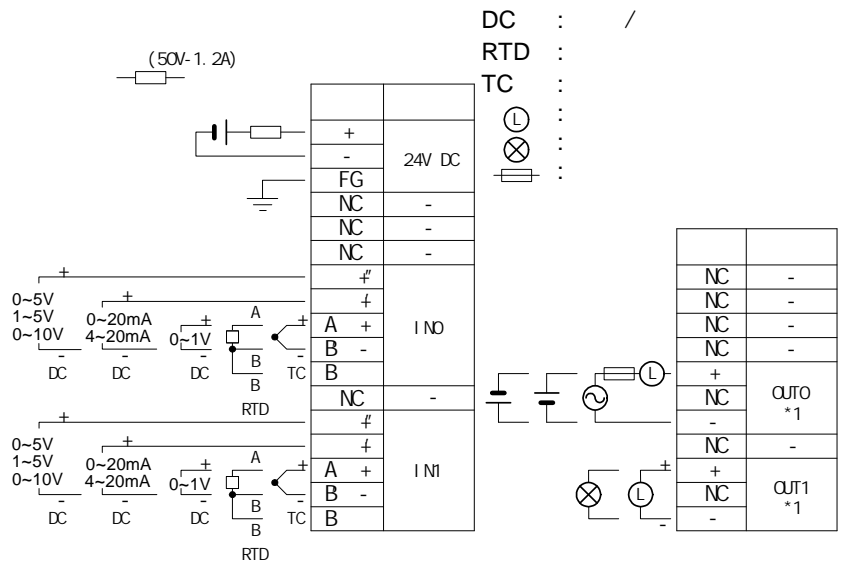
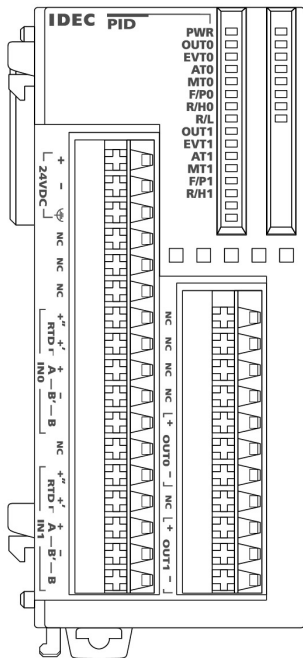
MicroSmart

60V DC 42.4V DC

PID

AVG16

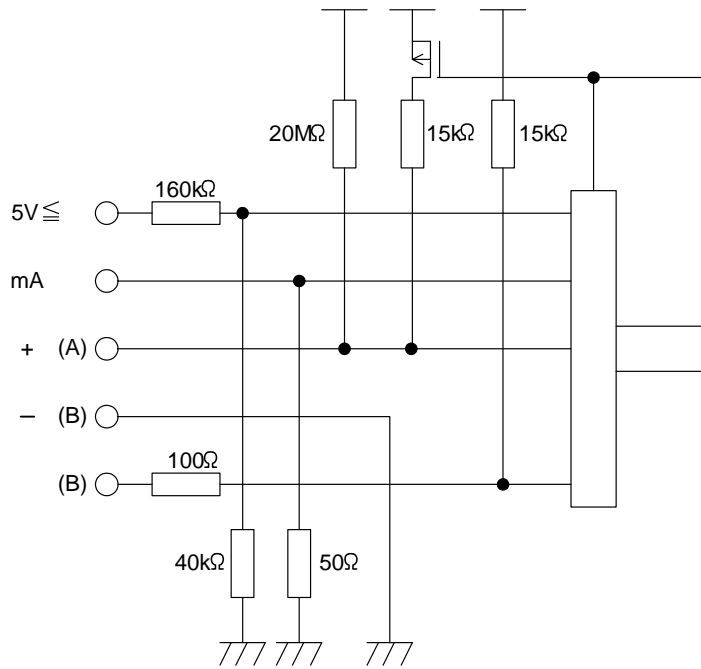
AVG18 AVG22 /



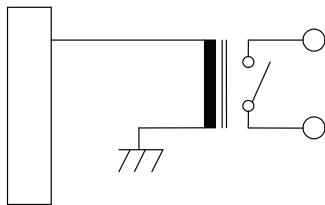
*1: OUT0
OUT1 /

PID

FC5A-F2MR2 FC5A-F2M2



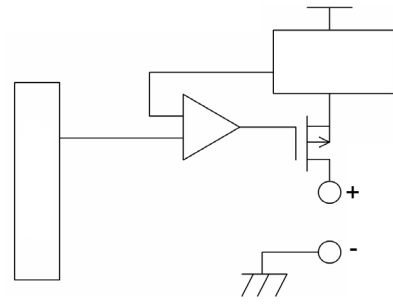
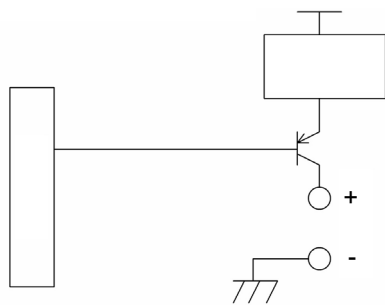
FC5A-F2MR2



FC5A-F2M2[

SSR]

FC5A-F2M2



PID

PID

MicroSmart CPU PID

MicroSmart CPU

PID

PID

5

0001h

PID

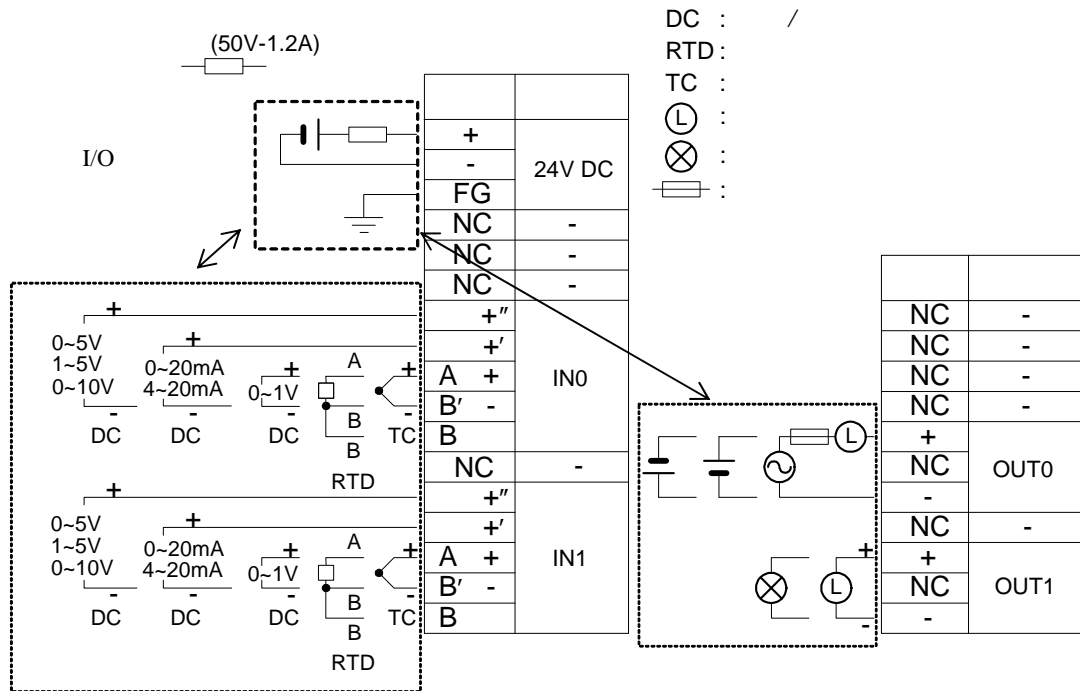
MicroSmart CPU

CPU

PID

I/O

I/O



4: PID

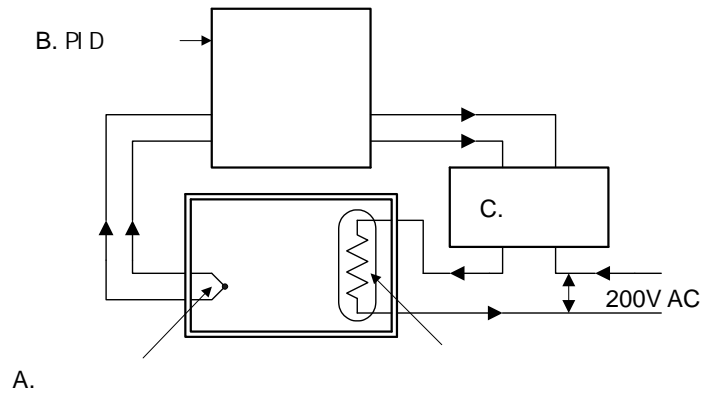
AT

/

PID

PID

PID



A.

B. PID

SP

PV

MV
MV

PV

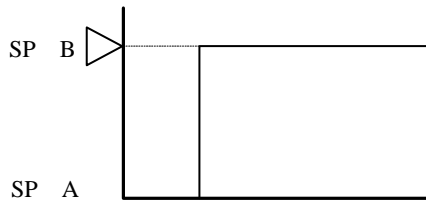
C.

PID

SSR

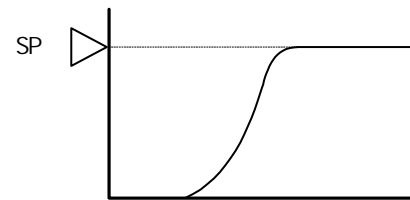
SP

1

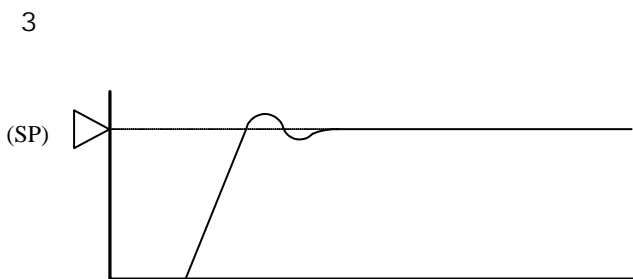
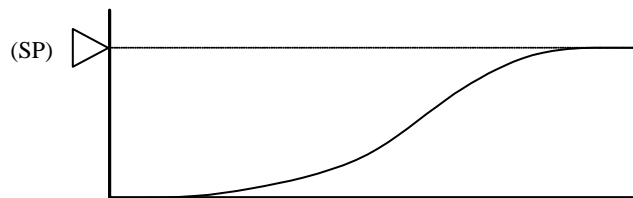
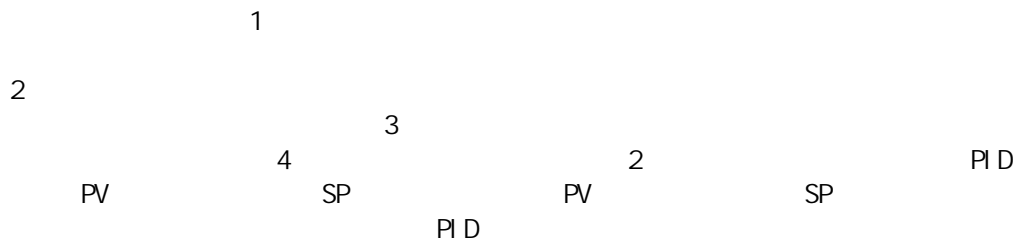


1

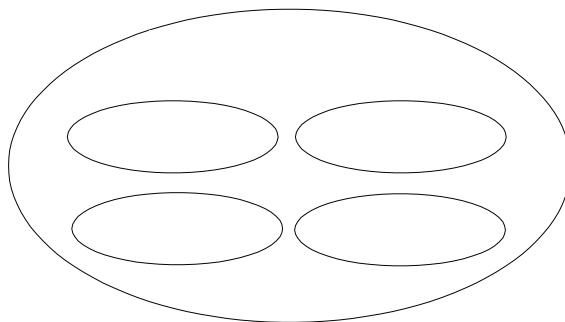
SP



2



- 1.
- 2.
- 3.
- 4.



PID 2

4-9 SP

PV SP PV SP

/

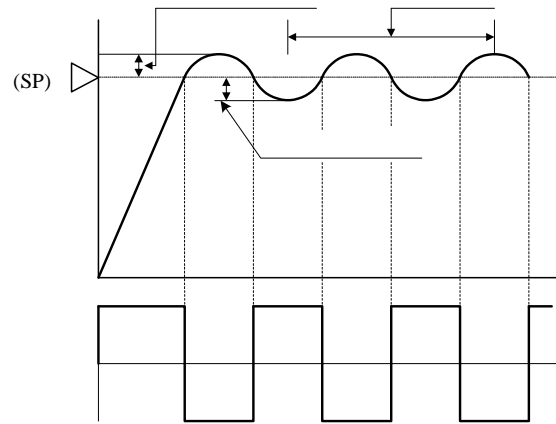
/ SP
PID

PV SP / PV

0 /

PV SP

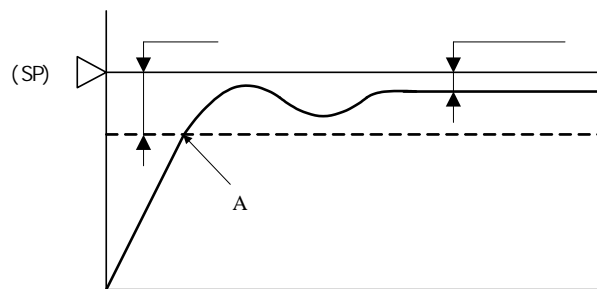
PV SP



P

P

PV PV SP PV A MV
PV A MV / PV A MV
" " SP " " SP
P P
PID 0 P

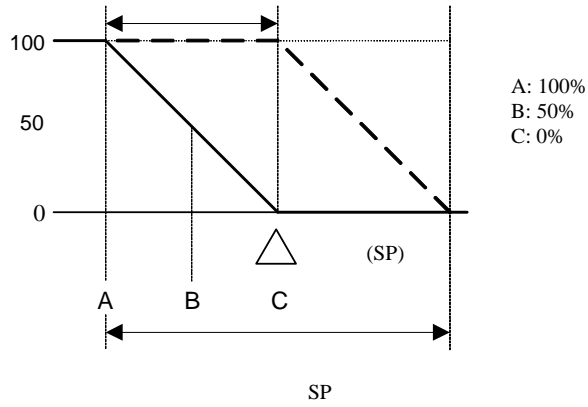


•

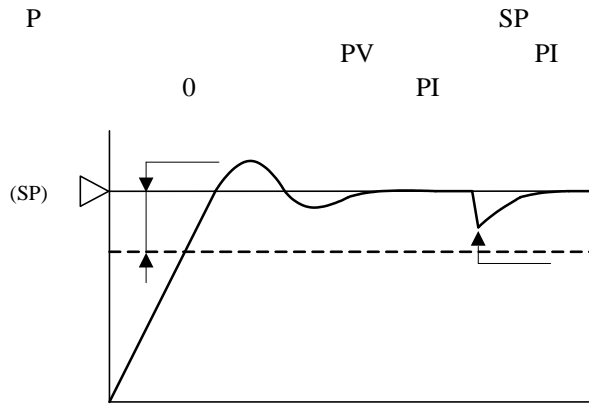
SP / PV / SP

•

PV / SP / PV / SP
P



• PI +
I PV
PID



•

•

• PD

P

+

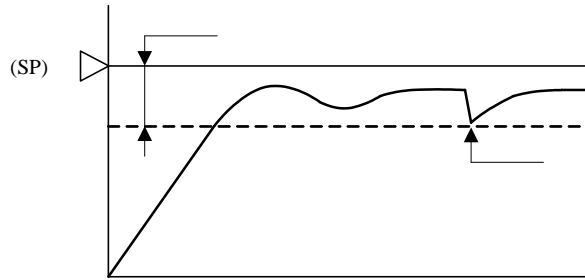
PD

0

PD

PD

PID



•

PV

SP

•

PV

SP

PD

• PID

P

+

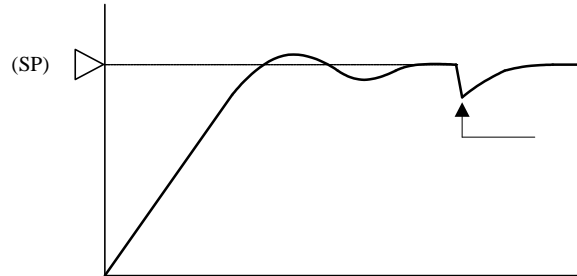
+

D

ARW


(AT)

PID



(AT)/

ARW (AT) P PD PID



- (AT)/
- (AT) P I D ARW
- AT
- (PV)
- (AT)/ (AT)/
- (AT) AT
- (SP)
- (AT)

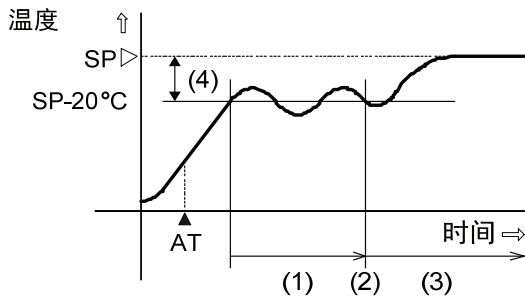
(AT)

(AT) (AT) P I D ARW (AT) (PV) (SP) AT

(AT) (SP) (AT) AT

[(PV) (SP) - AT]

AT 20°C PID (SP) 20°C



(1) PID

(2) PID (AT) PID

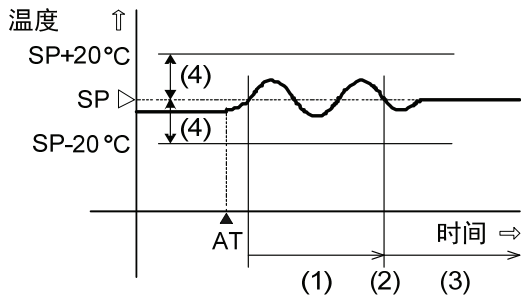
(3) (AT) (AT) PID

(4) AT (20°C)

AT (AT)

[(SP) - AT < (PV) < (SP) + AT]

(PV) (SP) PID



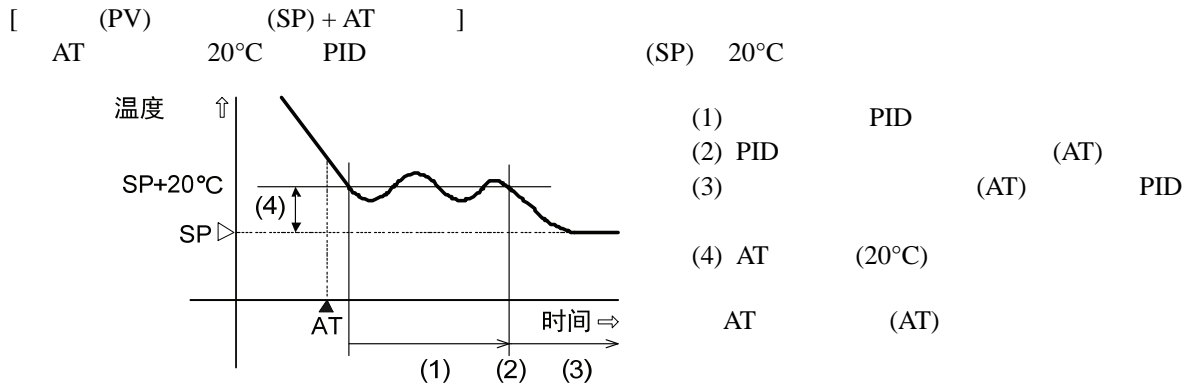
(1) PID

(2) PID (AT) PID

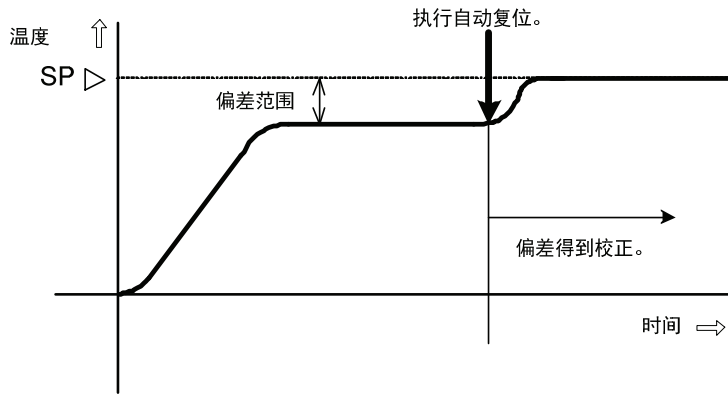
(3) (AT) (AT) PID

(4) AT (20°C)

AT (AT)



P PD (PV) (PV) (SP)
 PID CPU
 (P) 0 0.0



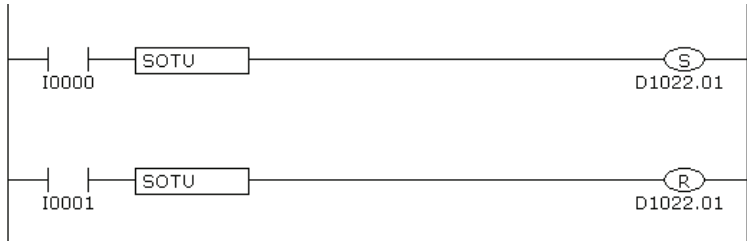
(AT)/ / (AT)/
 / 5-9

(AT)
 (AT) / (Bit0) (AT)/ (Bit1) P I
 D ARW (AT) P I D ARW (AT)
 (AT)/ LED (AT0/AT1) (AT)
 (AT) Bit1 CPU PID AT
 CPU PID PID

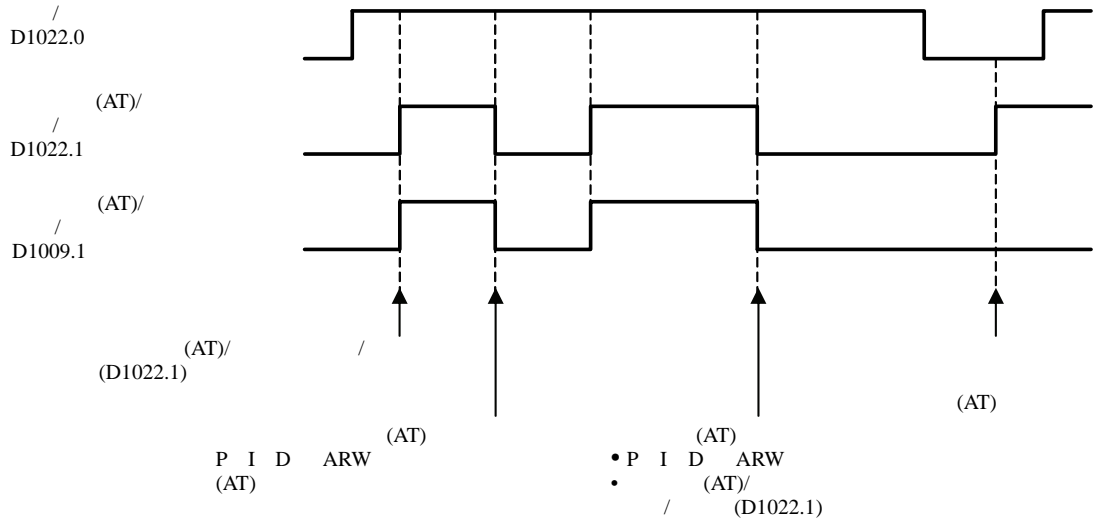
(AT)
 (AT) (AT)/ (Bit1) Bit1
 (AT) (AT)/ LED (AT0/AT1) (AT)
 P I D ARW (AT)

(AT)/ (Bit1)
 (AT)/ LED (AT0/AT1)

(AT)/ CH0 (AT)/ D1000
 M1000



I0 CH0 Bit1
 (AT)/
 I1 CH0 Bit1
 (AT)



- (AT)/ (AT)/ SOTU SET
- (AT)/ (AT)/
- (AT)/ (AT)/
- (AT) P I D ARW (AT)
-

(SP)

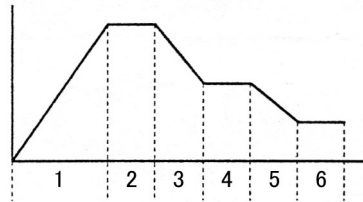
(SP)

(SP)

(PV)

10

(SP)



1 10

(SP)

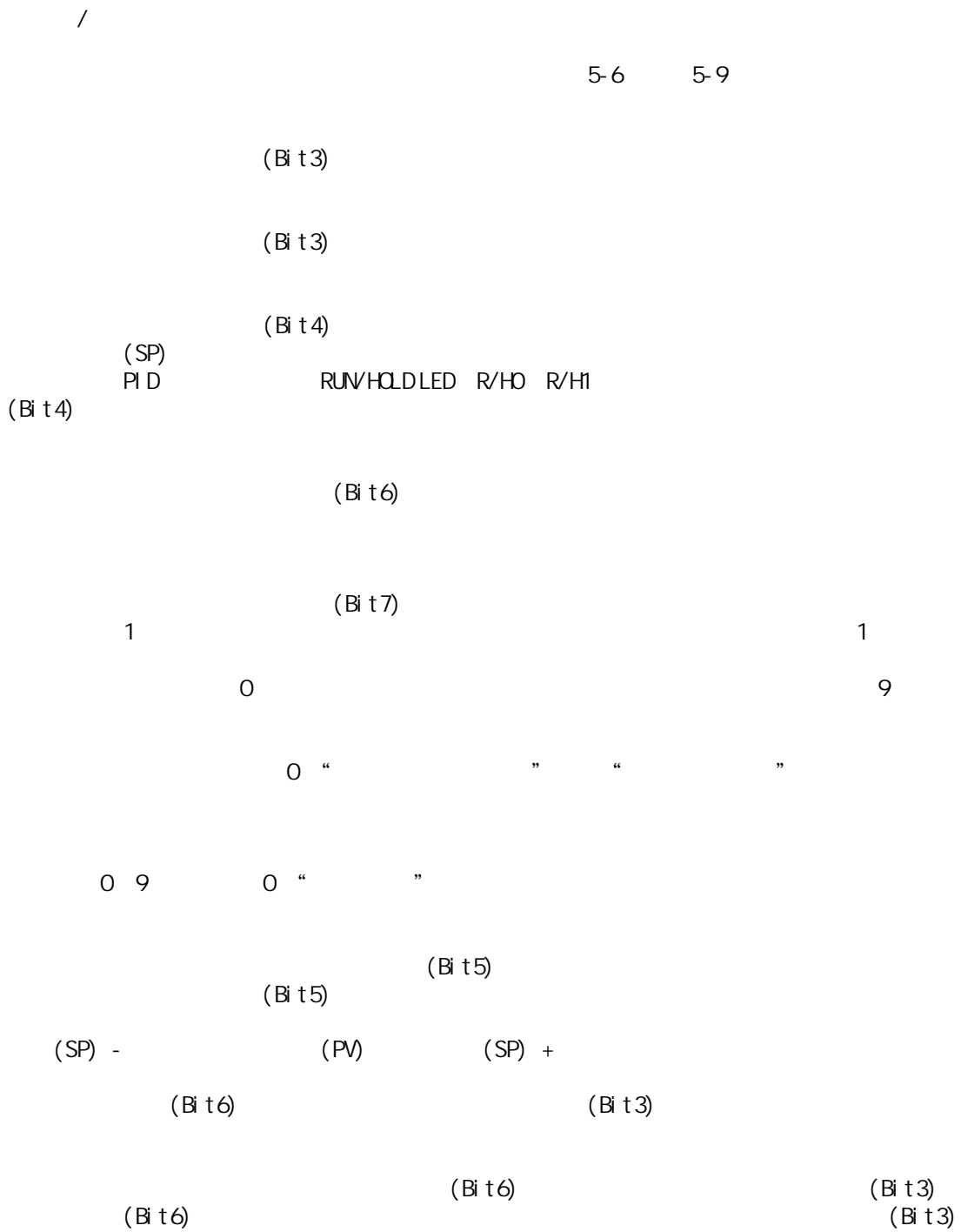
(PV)

(SP)

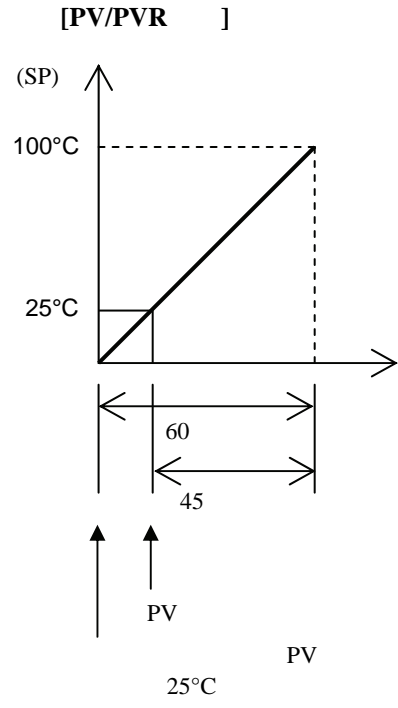
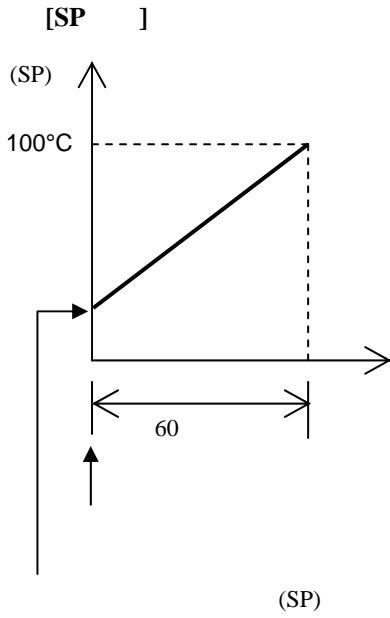
(PV)

(SP)

0



3 SP PV PVR SP (SP) (SP) PV
 PVR (SP) 100°C 6-35 60 (PV) (SP) (PV) 25°C



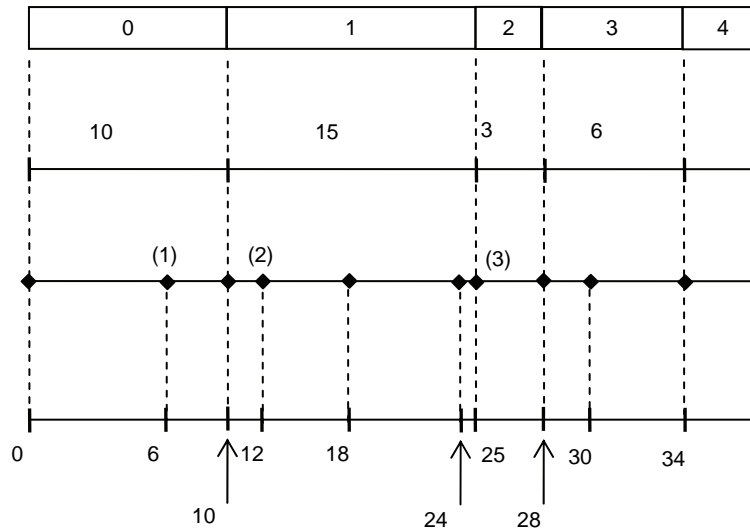
3
 0 9 “ ” PID “ ”
 0 “ ”
 6-37 9 (SP)

	PID	PID	PID	
	(*1)			
		*2 *3	*2 *3	0
				(SP)

*1:

*2:

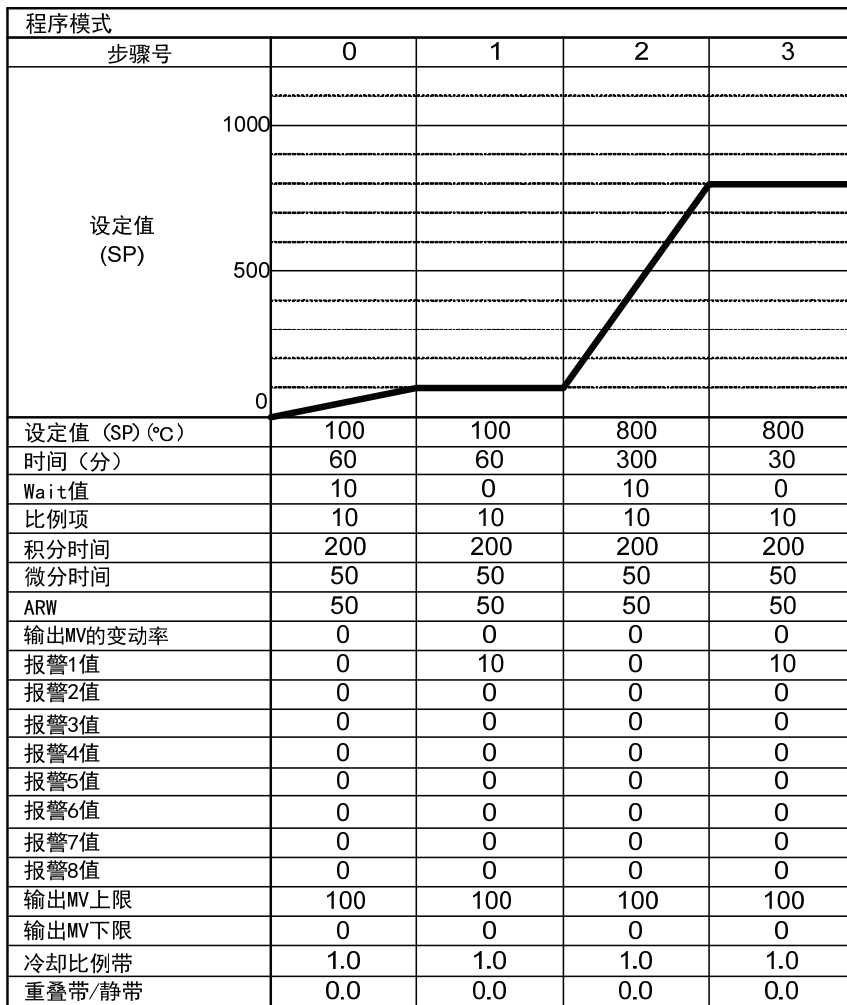
PID PID PID PID
 PID 6 PID



0 7 PID (1)
 1 4 PID PID (2)
 2 2 2 PID PID (3)
 *3 0 3

(SP)

(SP)

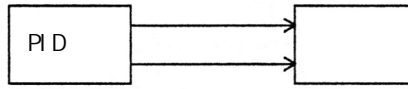


- [0] (SP) 60 100°C 1 (PV) 90°C
- [1] (SP) 100°C 60
- [2] (SP) 5 800°C 3 (PV) 790°C
- [3] (SP) 800°C 30

/

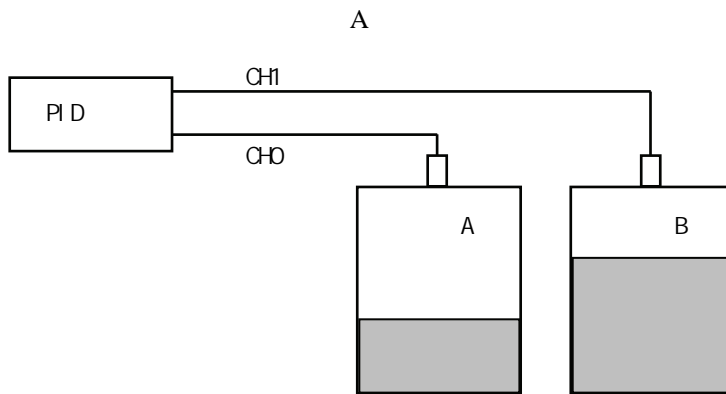
(PV) (CH0) (CH1) / (PV) (SP) (PV) (SP) (PV)

/

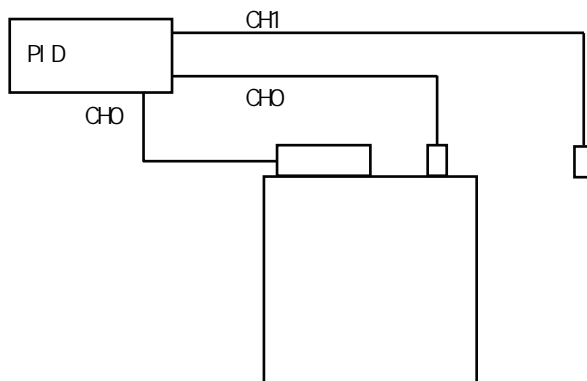


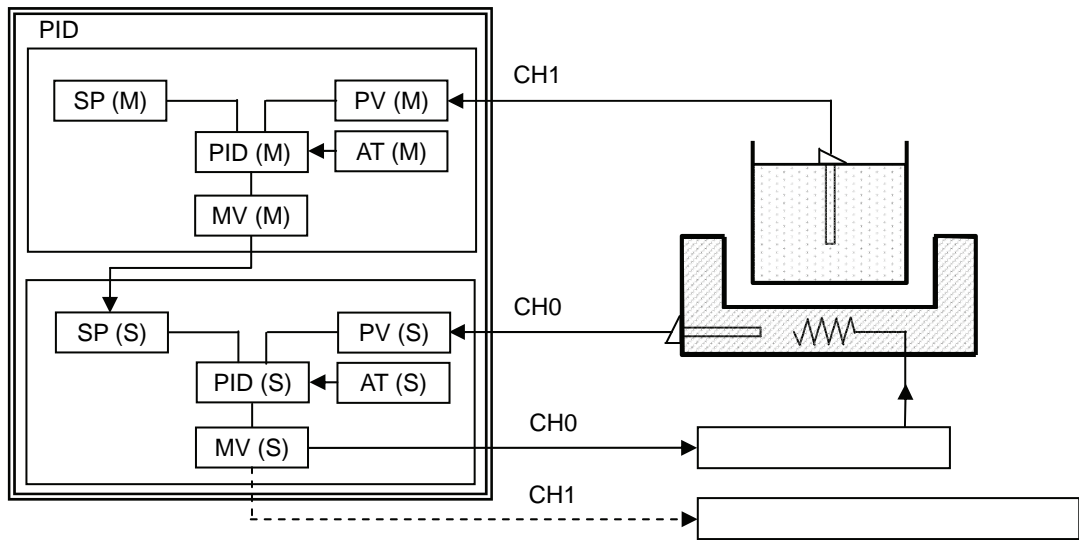
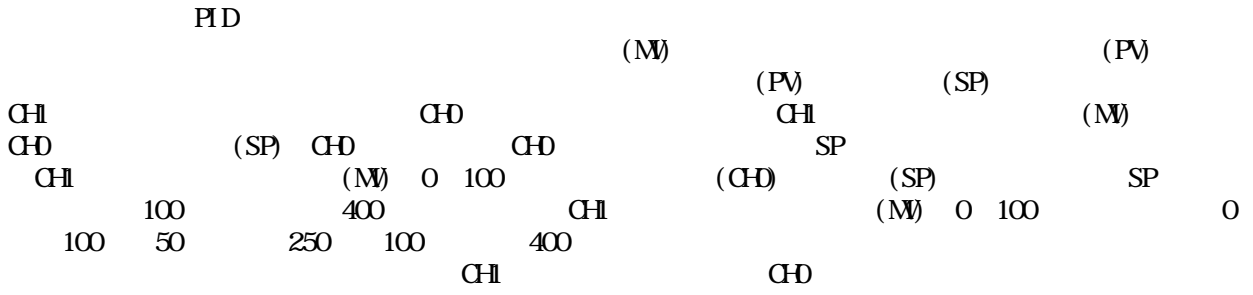
CH1 CH0 CH1 (PV) PID CH0 (SP)

1 2
PID 2

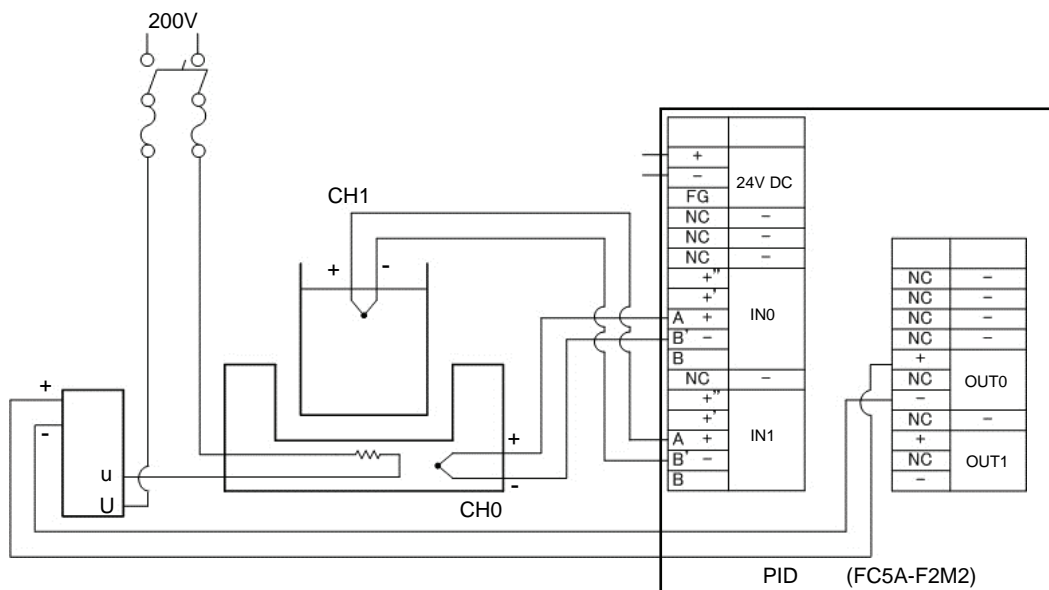


2
PID





FC5A - F2M2 []



(AI)

(AI)

(CH) (AI)

1. CHD CHI CHD CHI

2. (CH) (SP) (CH) (SP) SP

SP

3. CHD CHI CHD CHI CHD (AI) /

(CH) (AI)

(AI) P I D ARW

(CH) (AI)

4. CHI CHI

5. SP

6. CHD CHI (AI) / CHI (CH)

(AI)

(AI) (CH) P I D ARW

• (CH) (CH) (SP)

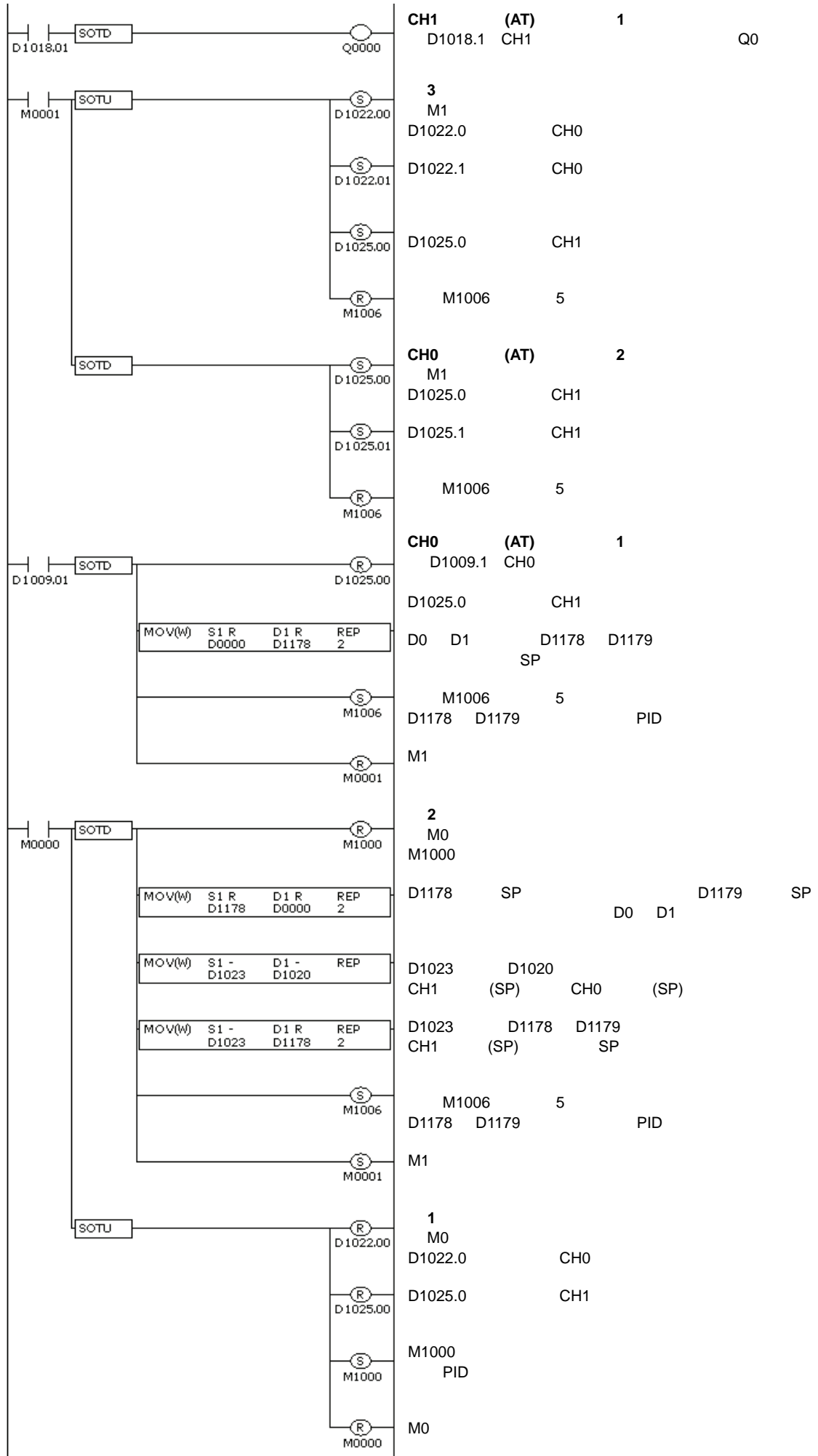
• (CH) (M) 0 100 (CH) (SP) SP

• SP (AI) P I D ARW (AI)

P I D ARW

(AI)

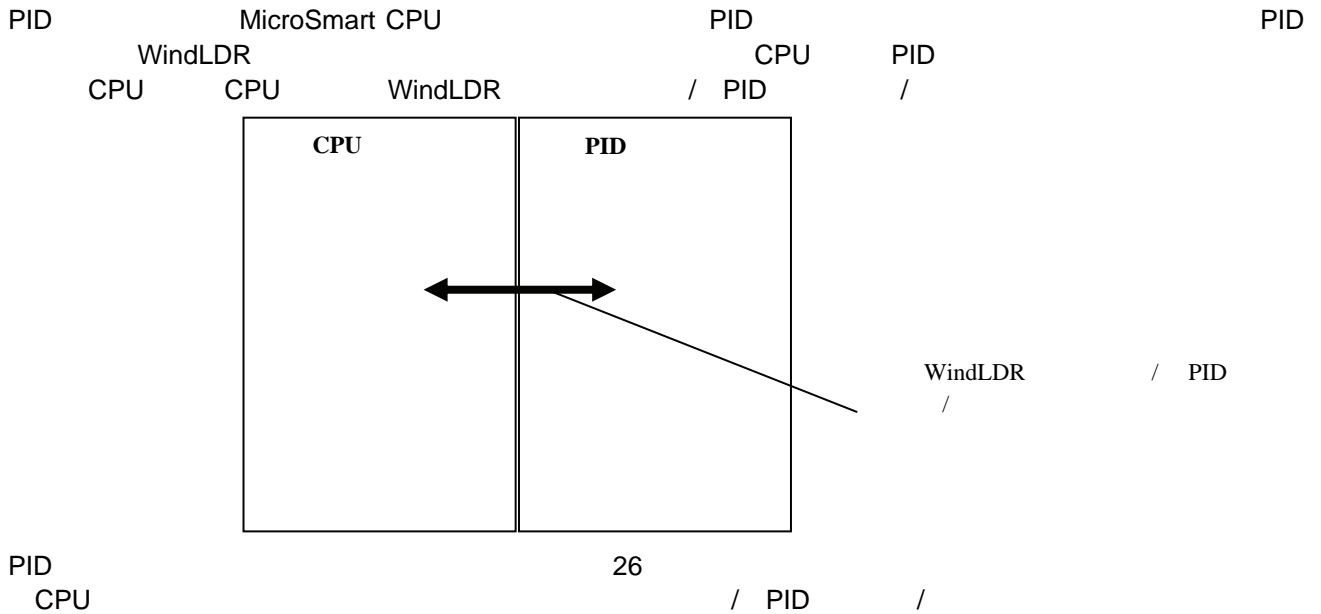
(CH) (CH) (AI)



5: PID

PID

PID



0	20	(CH0, CH1)
1	6	(CH0, CH1)
2	27	(CH0)
3	27	(CH1)
4	50	(CH0)
5	50	(CH1)
10 19	21/	(CH0)
30 39	19/	(CH1)

0 PID (PV) (SP) (MV) CPU
 PID 0 PID
 1 PID (SP) (MV) (AT)
 PID (SP) /
 2 3 PID / PID /
 4 5 PID
 10 19 30 39 / PID
 /

PID CPU PID

	CPU		PID	
	CH0	CH1	CH0	CH1
	1,300		4,400	
	1,200		3,900	

PID PID

	I	Q	M	R	T	C	D	
	-	-	-	-	-	-	X	-
	-	-	X	-	-	-	-	-

PID 590 (190)
 CH0 CH1 190
 CH0 CH1 590

PID CPU
 2000 (D0 D1999) CPU PID CPU
 PID

1 2 2000

1 PID

		CPU		
		CH0	CH1	
PID	FC5A-F2MR2/FC5A-F2M2			590
	FC5A-F2MR2/FC5A-F2M2			590
	FC5A-F2MR2/FC5A-F2M2			590
	FC5A-F2MR2/FC5A-F2M2			590
				2360

2 PID

		CPU		
		CH0	CH1	
PID	FC5A-F2MR2/FC5A-F2M2			590
	FC5A-F2MR2/FC5A-F2M2			590
	FC5A-F2MR2/FC5A-F2M2			590
	FC5A-F2MR2/FC5A-F2M2			590
				2360

3 4 2000

3 PID

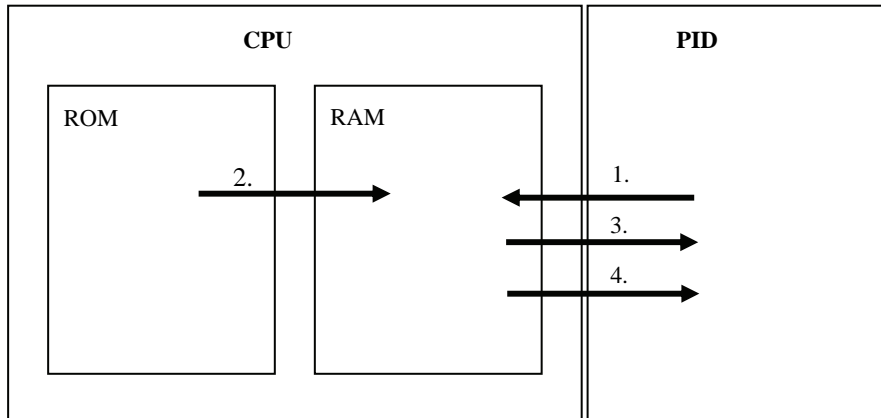
		CPU		
		CH0	CH1	
PID	FC5A-F2MR2/FC5A-F2M2			590
	FC5A-F2MR2/FC5A-F2M2			590
	FC5A-F2MR2/FC5A-F2M2			590
	FC5A-F2MR2/FC5A-F2M2			190
				1960

+27	39 CH1 9	/
+28		/
+29		/
+30		/
+31		/

5-7 5-24

- +0 ROM PID CPU
- +1 CPU PID CPU ROM
- +2 ROM (RAM) PID ROM
- +3 +27 2 5 10 19 30 39 PID ROM

PID



- 1. PID CPU ROM CPU
- 2. CPU ROM CPU RAM
- 3. CPU RAM PID
- 4. CPU RAM CPU

- CH0 CH1 CPU PID + 189
- CH0 CH1 + 589

0			
1		MicroSmart	PID
3		PID	MicroSmart PID

PID

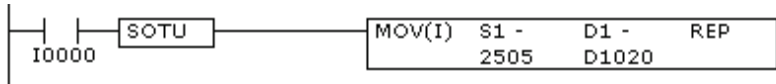
1 5 10 19 30 39
5-7 5-24

PID

1 **1**
CH0 (D1020) (SP) 250.5°C D1000 M500

2505 D1020*¹ PID *²

IO CH0 (SP) 250.5°C

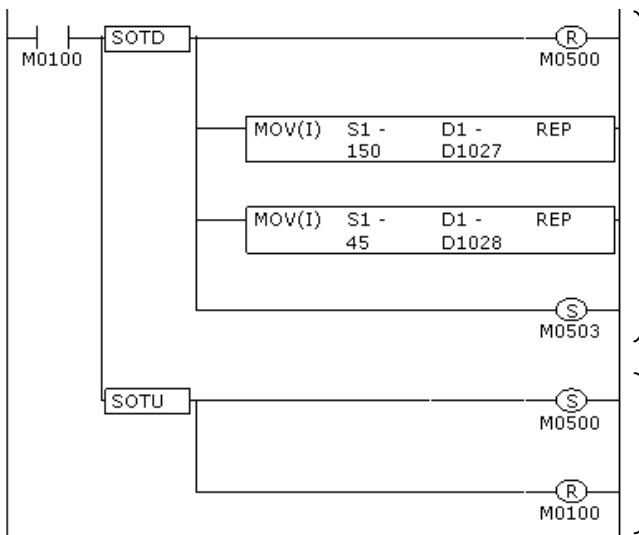


IO 2505
D1020[CH0 (SP)]

*1: 10
*2: D1000 1 D1020 D1025 PID

2 **2**
CH0 (D1027) 150 (D1028) 45 D1000
M500

1. M500() PID PID *¹
2. 150 D1027(CH0) 45 D1028(CH0)
3. M503(2)*² (150) (45) PID

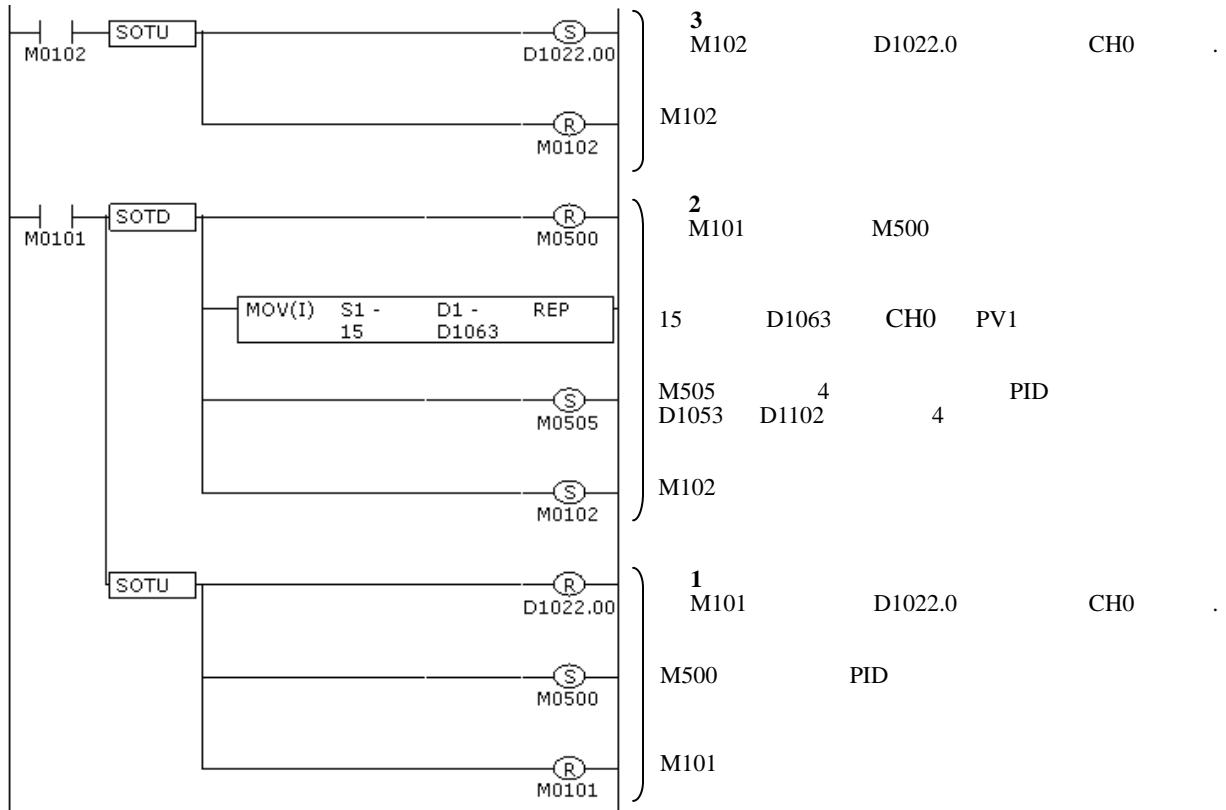


2
M100 M500
150 D1027 CH0
45 D1028 CH0
M503 2 PID
D1026 D1052 2
1
M100 M500 PID
M100

*1: (M500) PID PID 2
D1026 D1052
*2: D1026 D1052 2 PID

3 PV 4 CH0 (D1063) 1.5 D1000 M500

1. M500() PID PID CPU *1
2. D1022.0(CH0) PID CH0
3. 15 D1063 (CH0 PV1)²
4. M505(4)³
5. D1022.0(CH0) PID CH0



*1: (M500) PID PID 4
D1053 D1102

*2: 10

*3: D1053 D1102 4 PID

PID CH0 CH1 4 5 PID
4 5 PID

- 0

CPU	PID			
0		PID	0000h: 0001h: 0002h:	/
+1	CH0	(PV)	9-4	
+2		(MV)		
+3		(MV)		
+4		(SP)	(SP) (SP)	
+5			0 6000 /	
+6			0 9	
+7			0 10000	
+8			0	
+9				5-8
+10				5-9
+11	CH1	(PV)	9-4	
+12		(MV)		
+13		(SP)	(SP) (SP)	
+14			0 6000 /	
+15			0 9	
+16			0 10000	
+17			0	
+18				5-8
+19			5-9	

1			
0		0	
		1	
1	(AT) /	0	
		1	(AT) /
2	/	0	
		1	
3		0	
		1	
4		0	
		1	
5	CH0	0	
		1	
6		0	
		1	
7	(SP)	0	(SP)
		1	(SP)
8		0	
		1	
9	/ / / ARW /	0	ARW
		1	ARW
10		0	
		1	
11	/ CH0	0	
		1	
12	/ CH0	0	/
		1	/
13	1 8	0	1 8
		1	1 8
14	PV /PV	0	PV /PV
		1	PV /PV
15	(SP)	0	(SP)
		1	(SP)

PID

1			
0		0	OFF
		1	ON
1	CH0	0	OFF
		1	ON
2		0	
		1	
3		0	
		1	(9-4) (0 1V)
4		0	
		1	(9-4) (0 5V) (4 20mA)
5		0	
		1	
6		0	OFF
		1	ON
7	1	0	OFF
		1	ON
8	2	0	OFF
		1	ON
9	3	0	OFF
		1	ON
10	4	0	OFF
		1	ON
11	5	0	OFF
		1	ON
12	6	0	OFF
		1	ON
13	7	0	OFF
		1	ON
14	8	0	OFF
		1	ON
15		0	0

- 1

CPU		PID		
+20	CH0	(SP)	(SP) (SP)	/
+21			/	
+22				
+23	CH1	(SP)	(SP) (SP)	
+24				
+25				

PID 1

1			
0		0	
		1	
1	(AT)/ *1	0	(AT)/
		1	(AT)/
2	/	0	
		1	
3		0	
		1	
4	*2	0	
		1	
5	SPSP	0	SP
		1	SP
6	*3	0	
		1	
7	*4	0	
		1	
8		0	0
15			

- *1:
- *2:
- *3:
- *4:

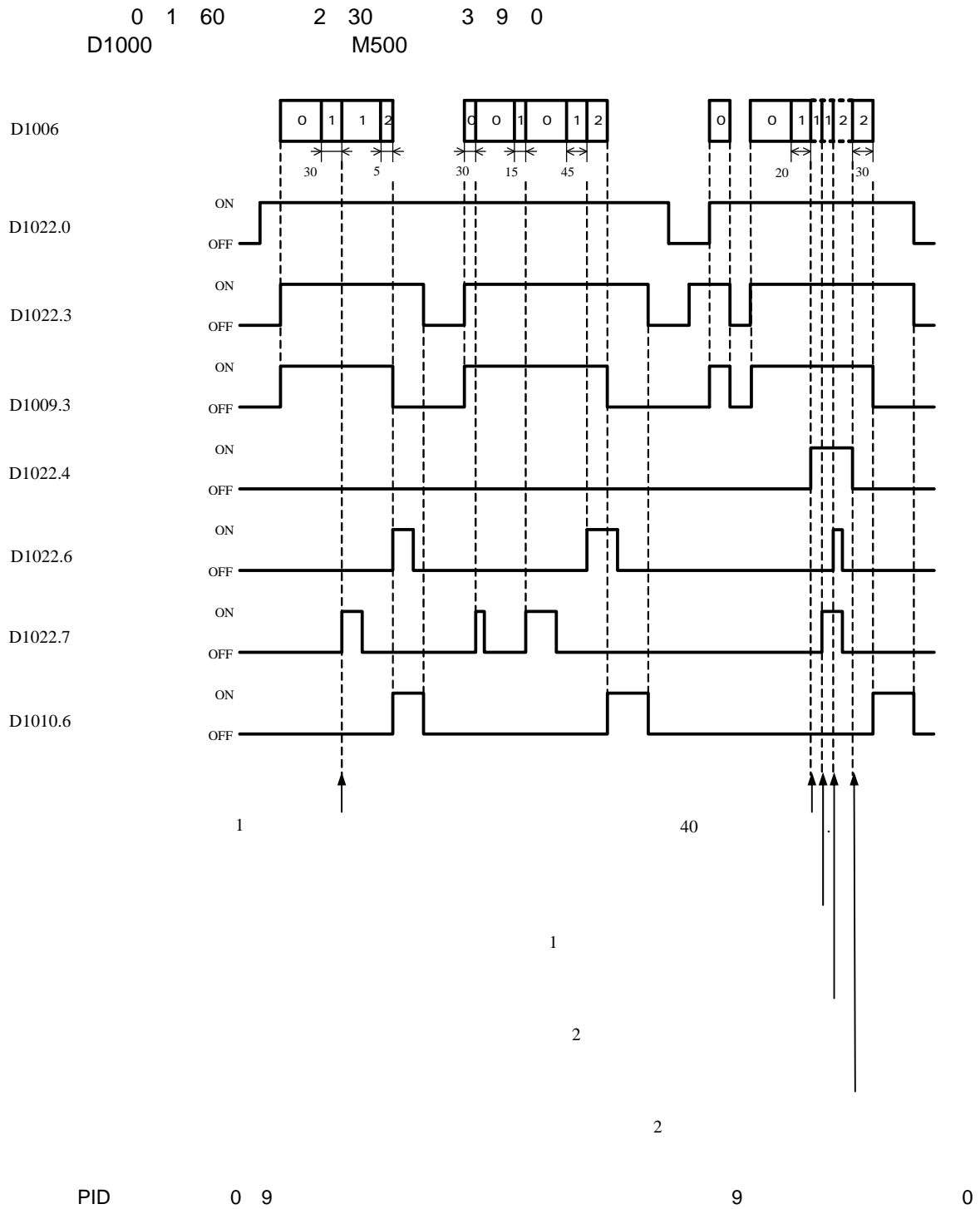
0

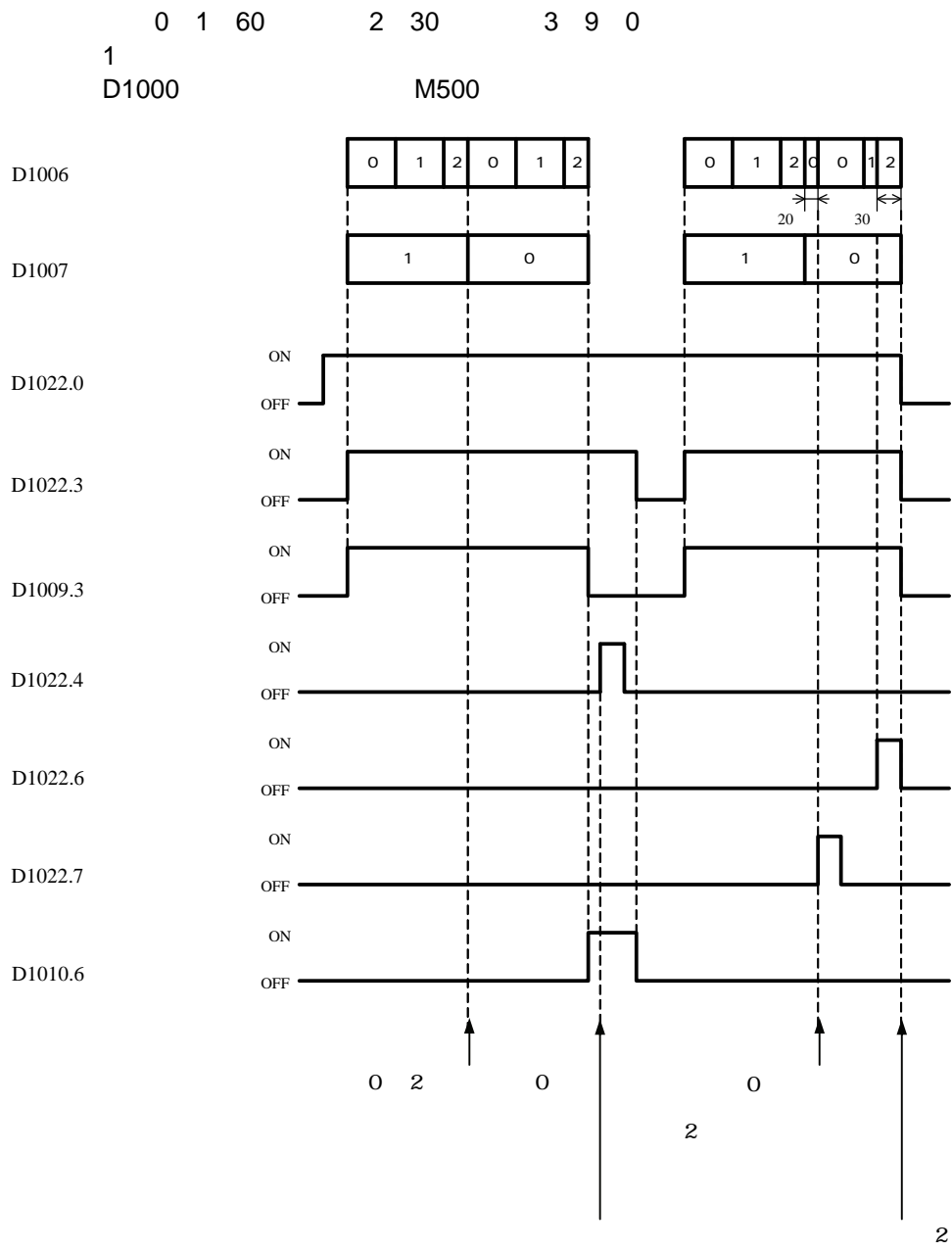
1

9

1

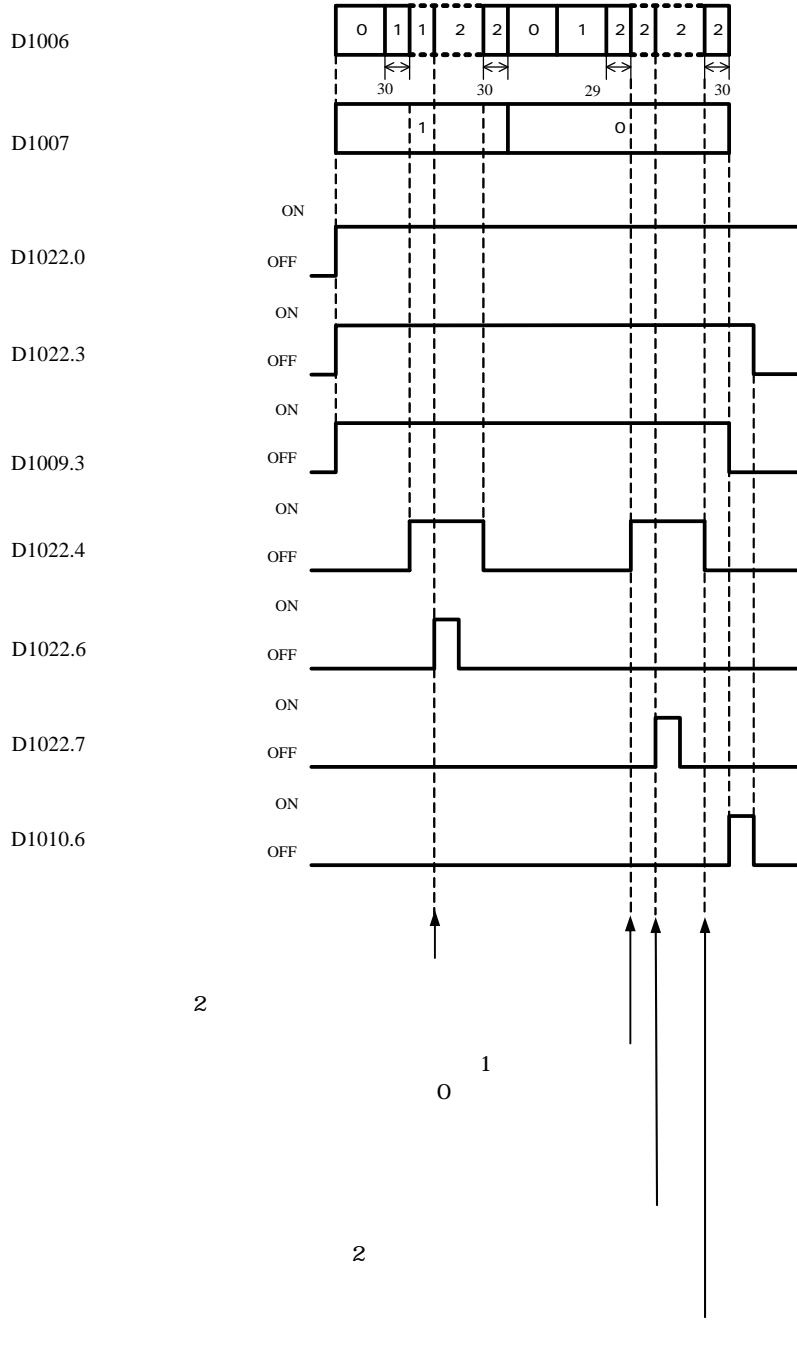
1

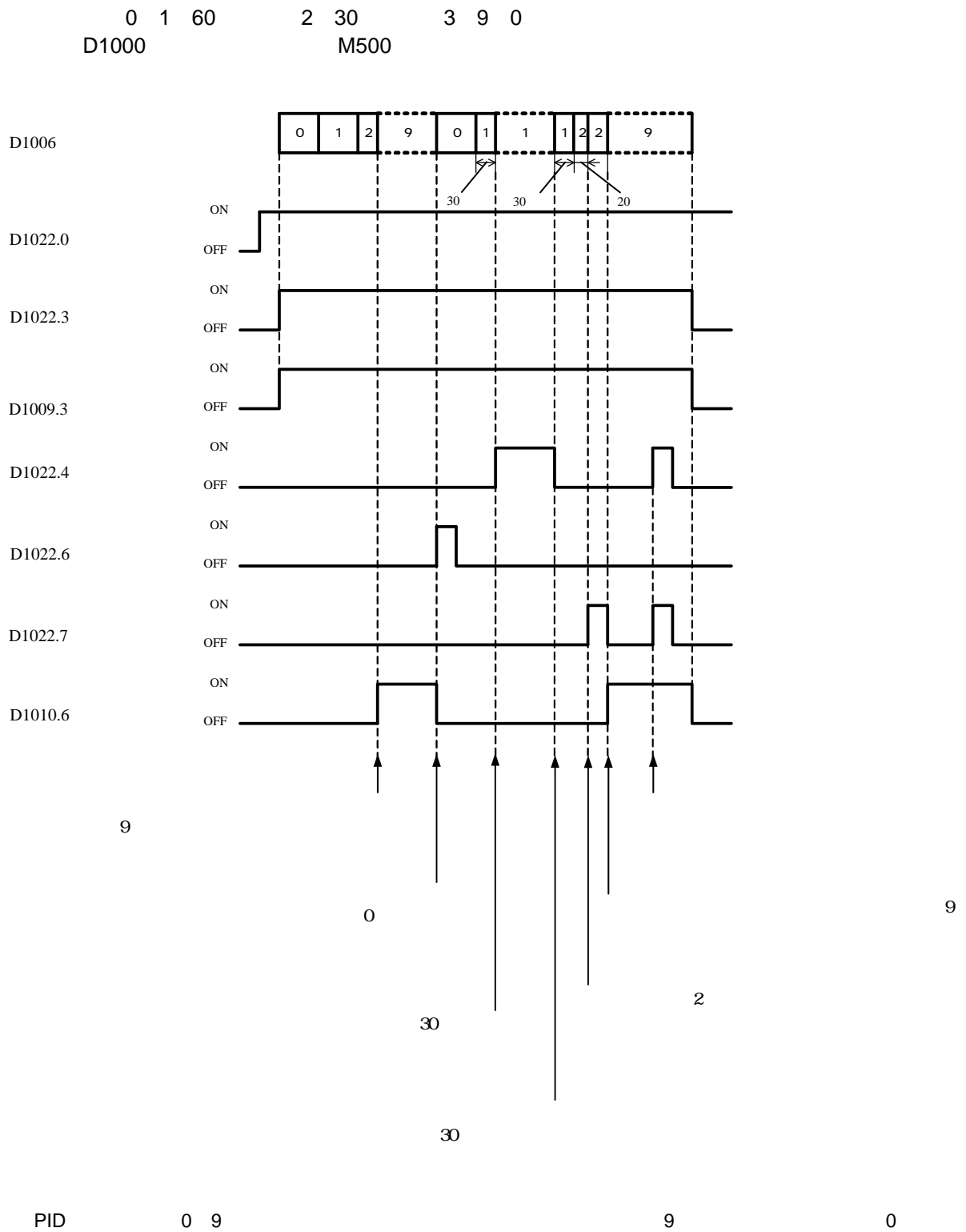




3

0 1 60 2 30 3 9 0
 1
 D1000 M500





0 1 60
D1000

2 30 3 9 0
M500

